

FORM PTO/SB/08A/B (10-01) Substitute for PTO-1449A/B	Attorney Docket Number	50580/PAN/B600
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application Number	To be assigned
	Filing Date	Filed herewith
	Applicant(s)	Robert A. Hawley et al.
	Group Art Unit	To be assigned
	Examiner Name	To be assigned

U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	Cite. No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	Publication Date MM-DD-YYYY	Name of Patentee
<i>RC</i>	1	5,412,352	05-02-1995	Graham
<i>RC</i>	2	5,479,457	12-26-1995	Waters
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<i>RC</i>	4	5,479,457	12-26-1995	Waters
<i>RC</i>	5	5,519,732	05-21-1996	Chester
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<i>RC</i>	7	5,625,646	04-29-1997	Goodson et al.
<i>RC</i>	8	5,724,396	03-03-1998	Claydon et al.
<i>RC</i>	9	5,748,126	05-05-1998	Ma et al.
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<i>RC</i>	13	5,828,710	10-27-1998	Beale, Terrance Ralph
<i>RC</i>	14	5,943,369	08-24-1999	Knutson et al.
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<i>RC</i>	16	6,041,088	03-21-2000	McCallister
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<i>RC</i>	18	6,067,319	05-23-2000	Copeland
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EXAMINER INITIALS	Cite No. ¹	FOREIGN PATENT DOCUMENT Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)

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OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	20	Henry Samuelli, et al., "VLSI Architectures for a High-Speed Tunable Digital Modulator/Demodulator/Band Pass-Filter Chip Set", Integrated Circuits and Systems Laboratory Electrical Engineering Department University of California, Los Angeles, (ISCAS '92), 4 pages. <i>Not Available</i>

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